CONTEXT EXECUTION IN A PIPELINED COMPUTER PROCESSOR

ABSTRACT

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A processing system that executes multiple instruction contexts includes an instruction memory for storing instructions that are executed by the system, a processor unit executing the instructions in a pipelined fashion, a plurality

10 of context registers for storing instructions and instruction addresses for contexts to be executed and fetch logic for selecting an address from one of the context registers and for selecting an instruction from a second of the context registers for execution substantially simultaneously for each

15 cycle of execution of processor unit.